

Fig. 1

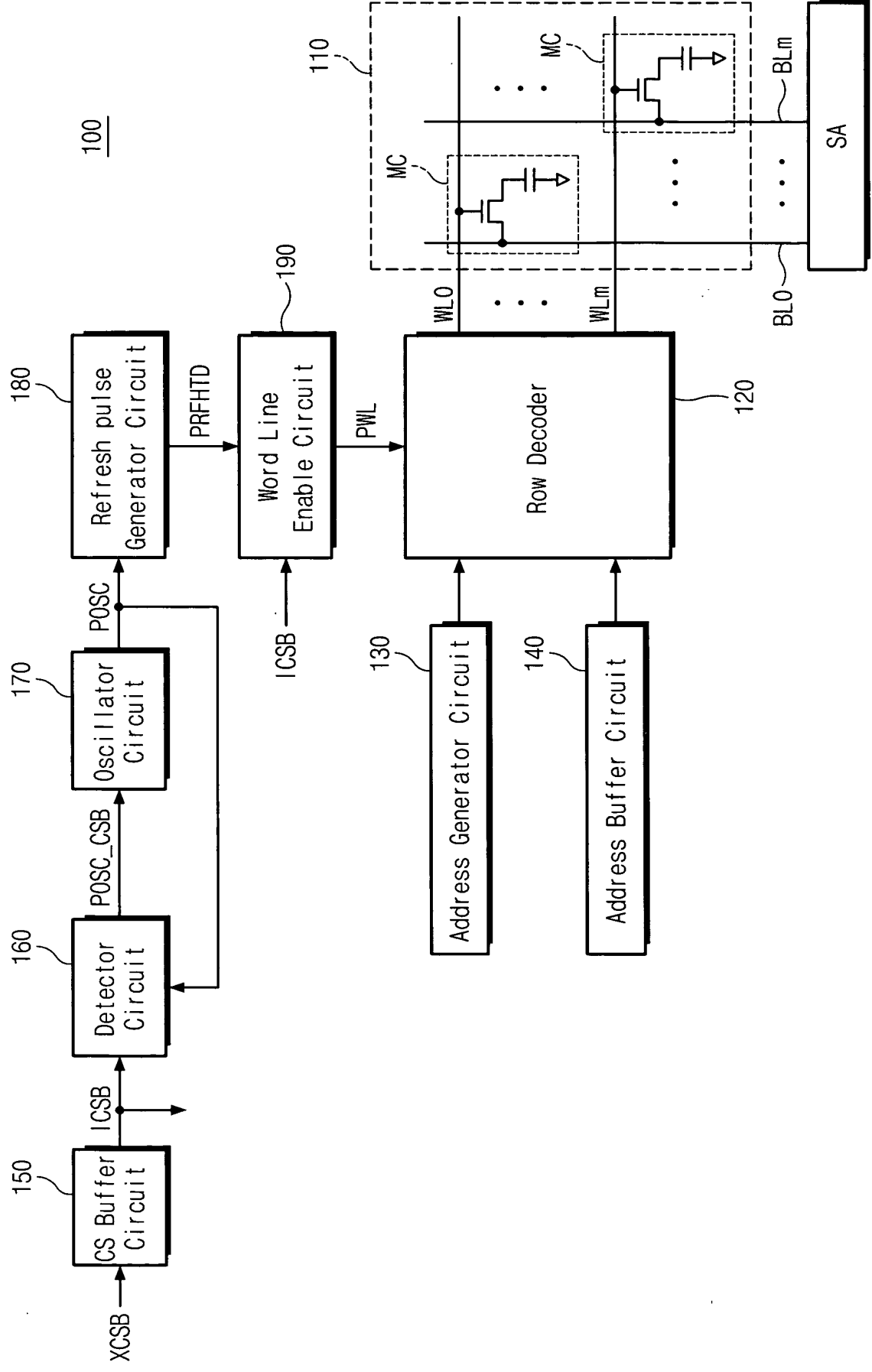


Fig. 2

160

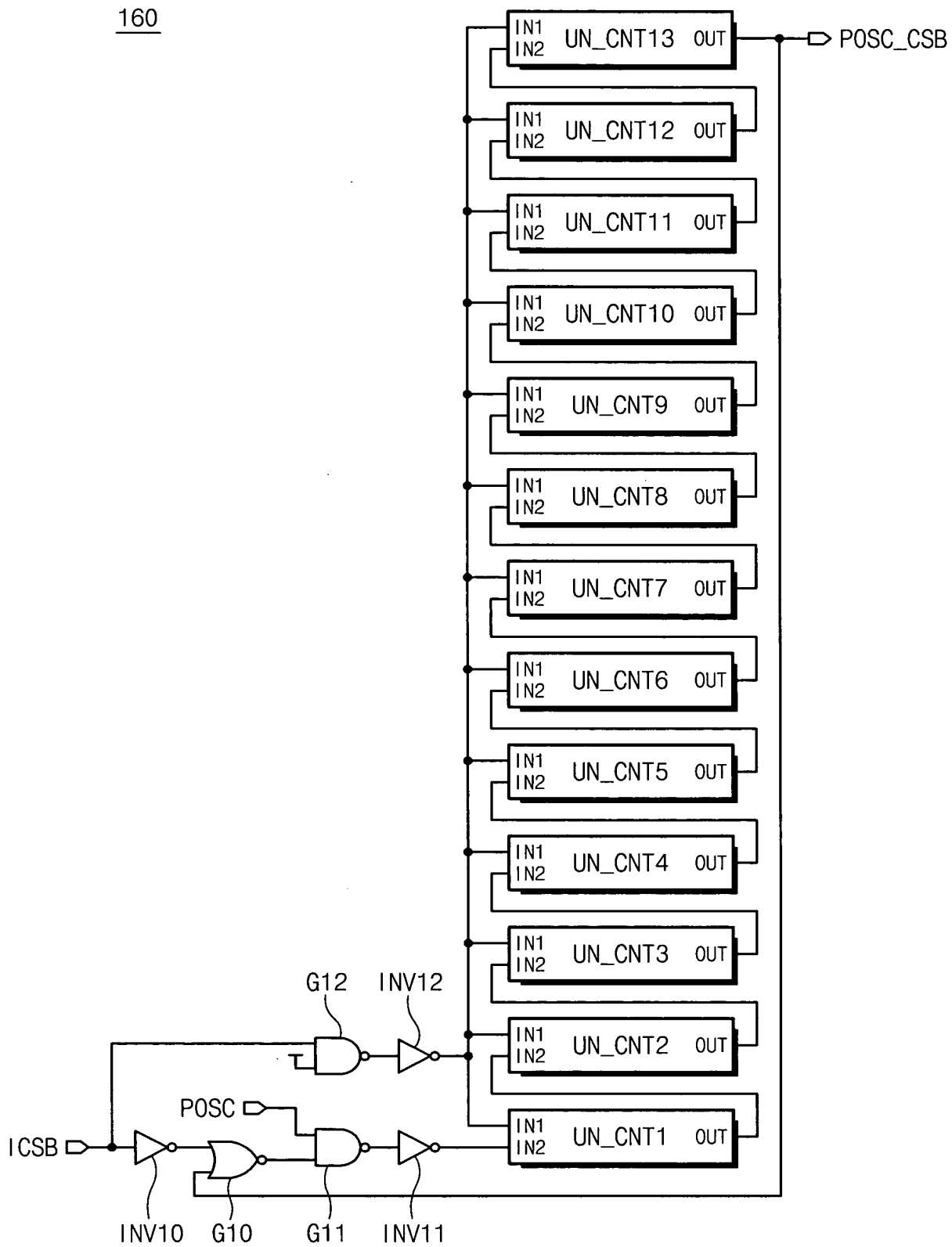
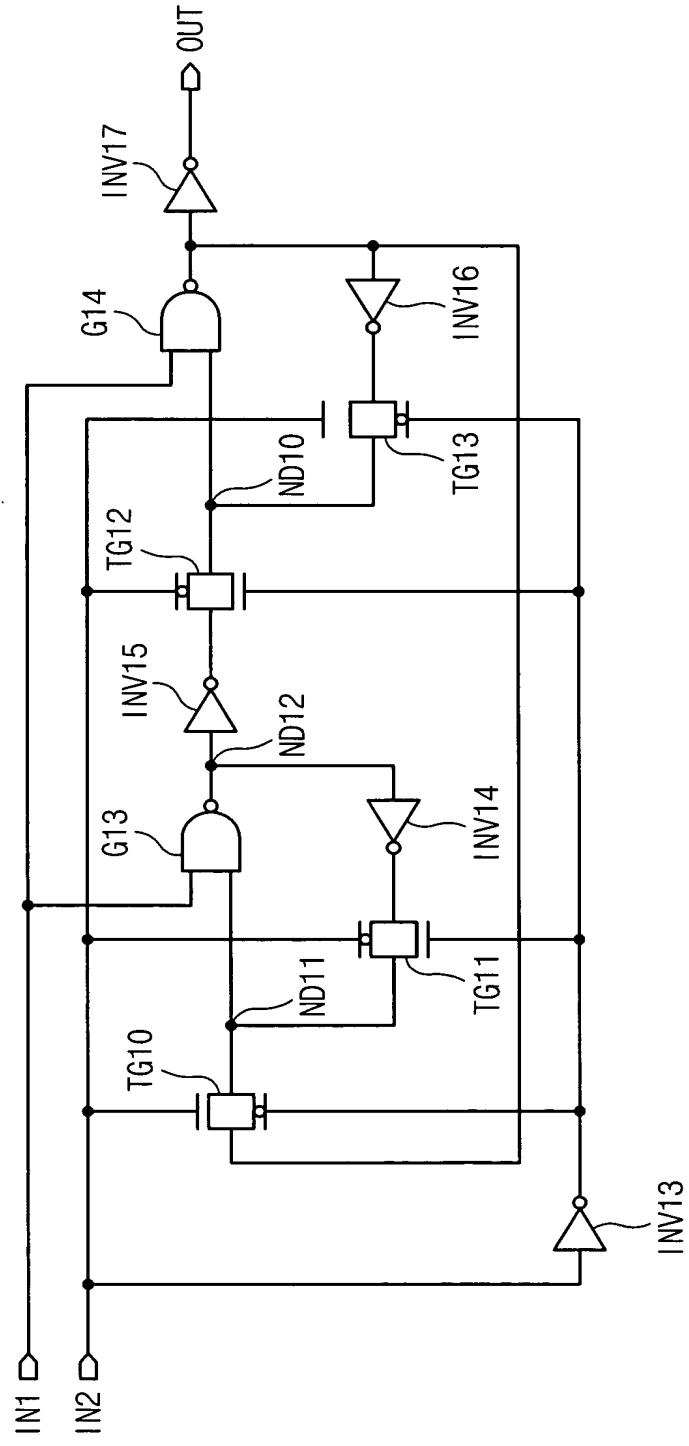


Fig. 3



UN_CNT1

Fig. 4

170

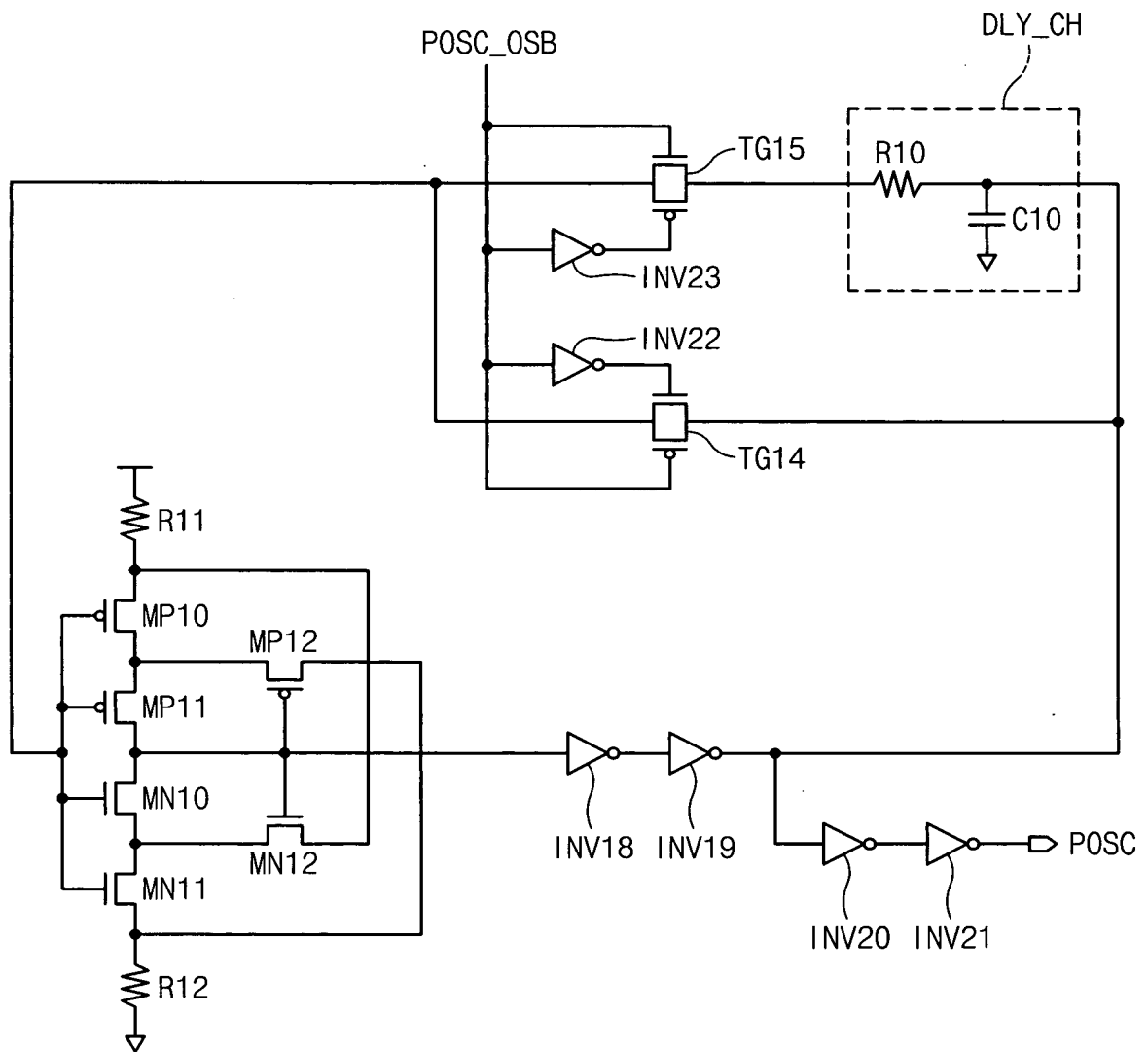


Fig. 5

180

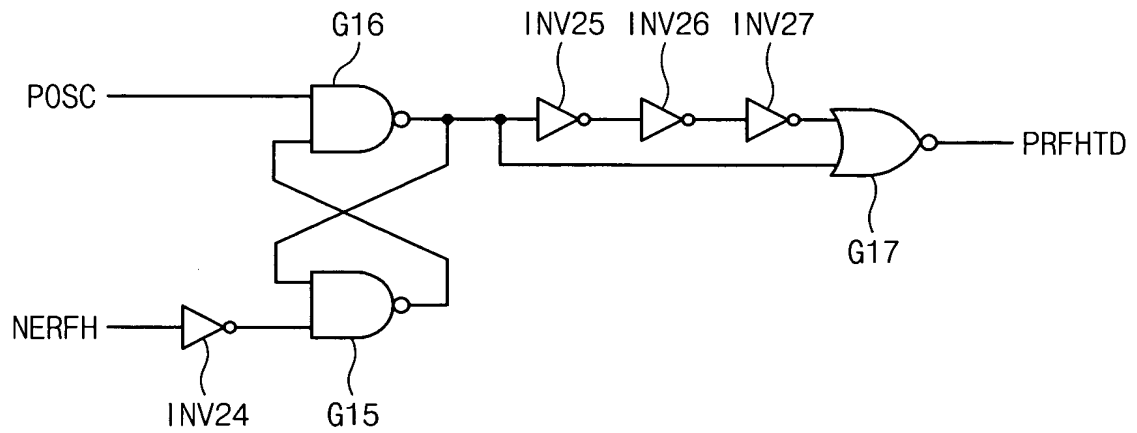


Fig. 6

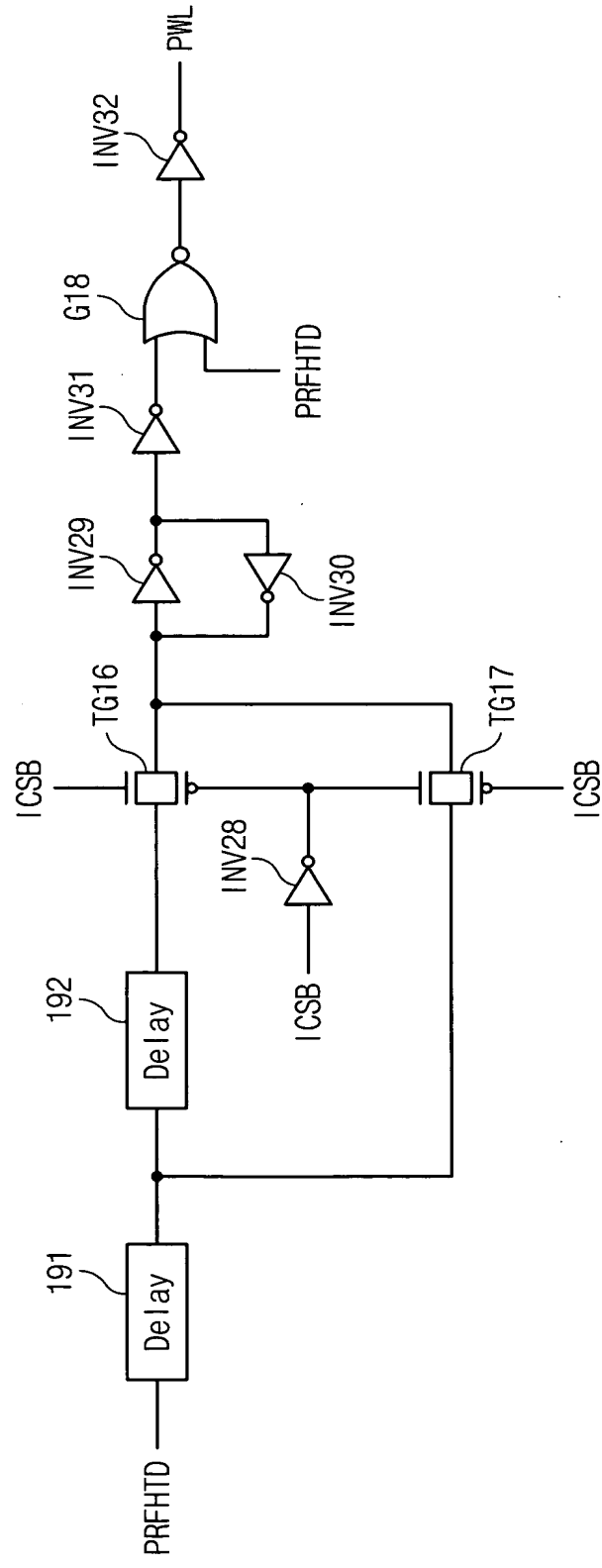


Fig. 7

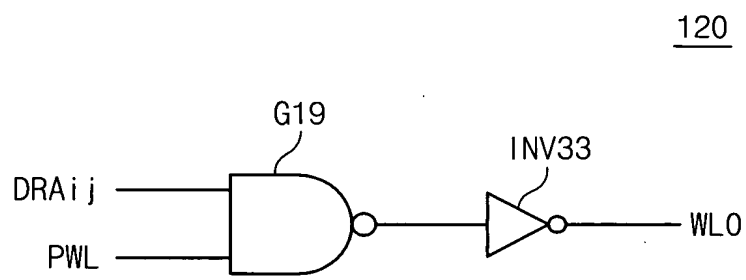
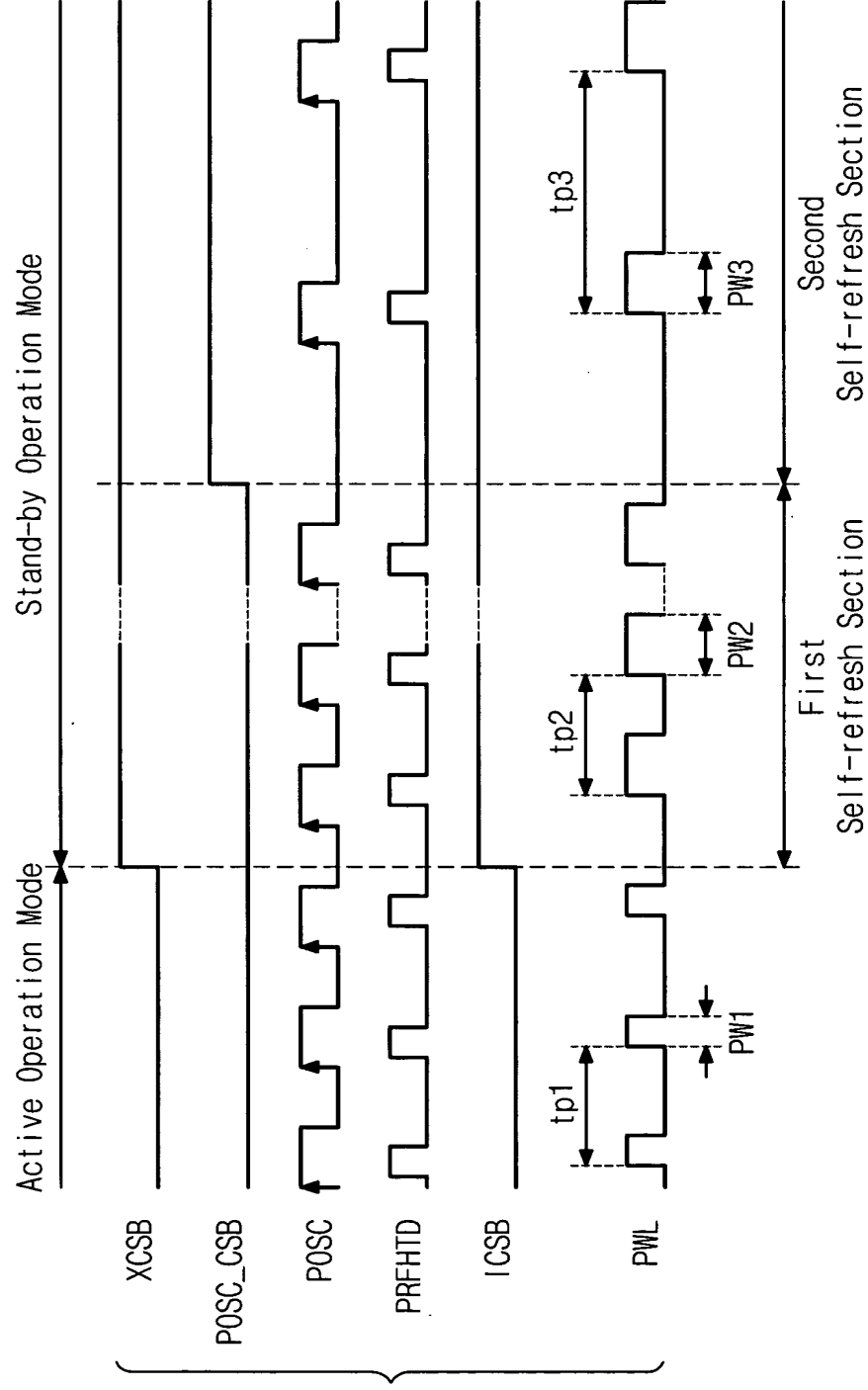


Fig. 8



Refresh Period : $tp1=tp2 \neq tp3$ ($tp3 > tp1$)
 Pulse Width of PWL : $PW1 \neq PW2 = PW3$ ($PW2 > PW1$)
 n : Arbitrary Addresses